

FIG. 1

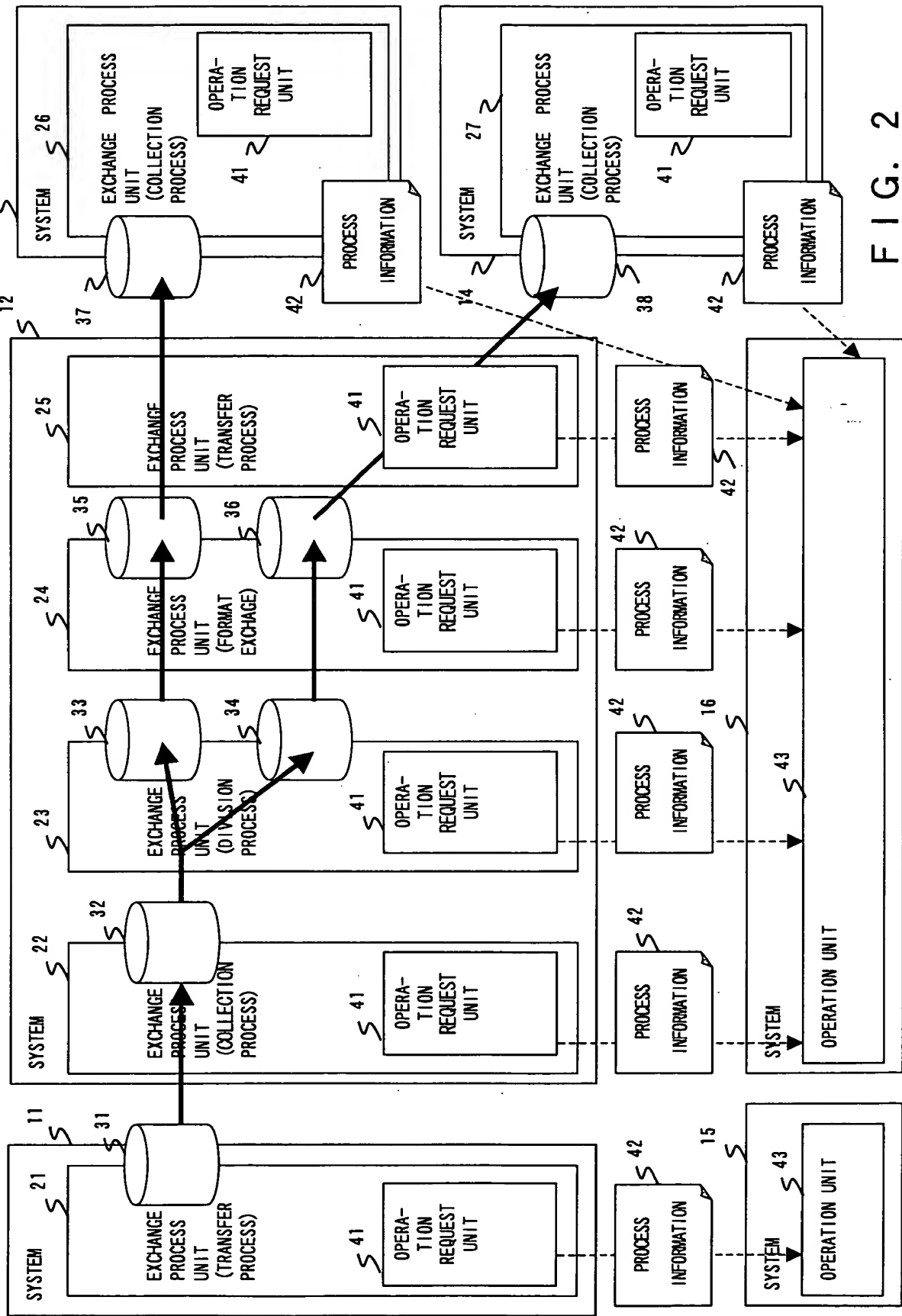


FIG. 2

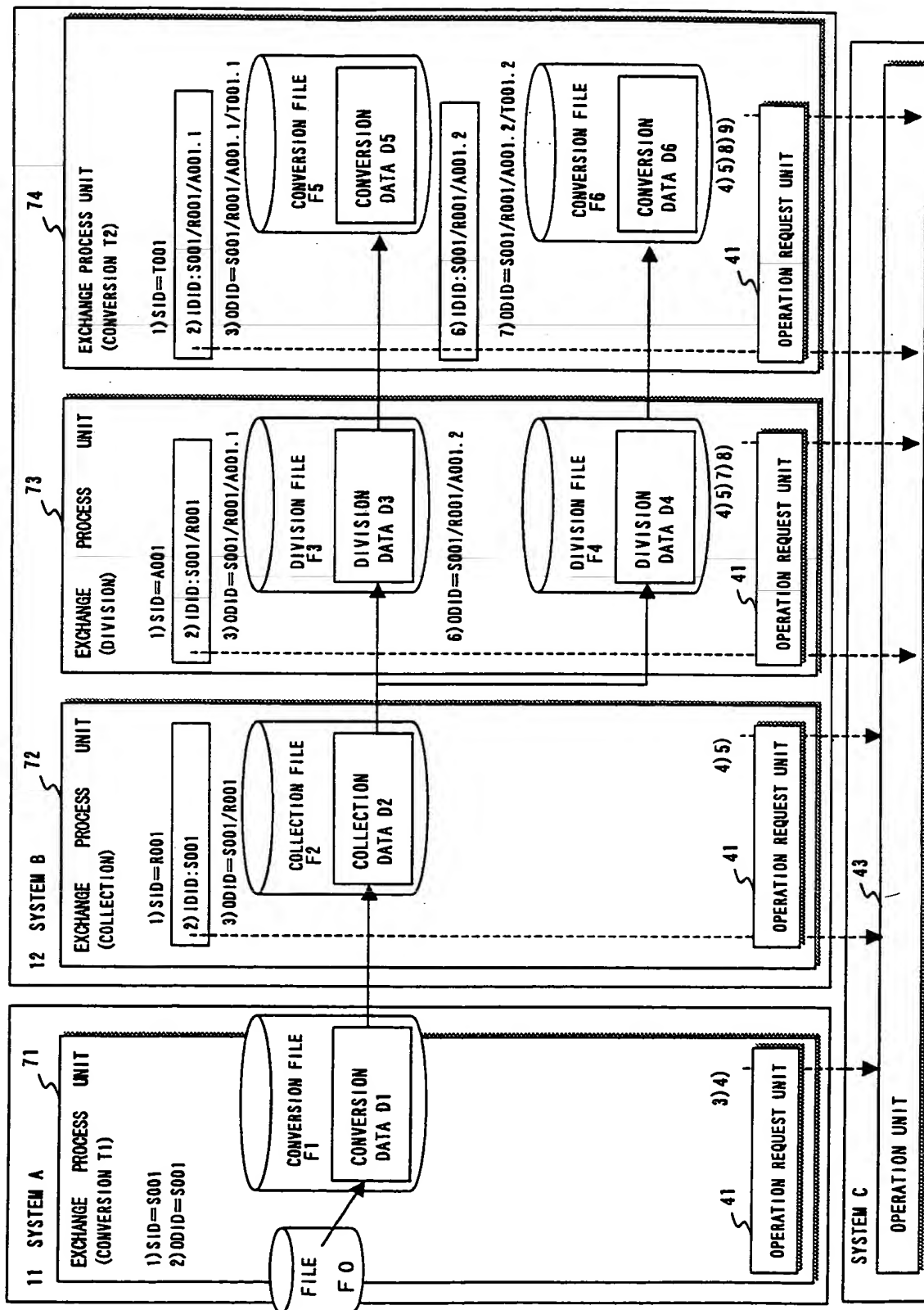


FIG. 4

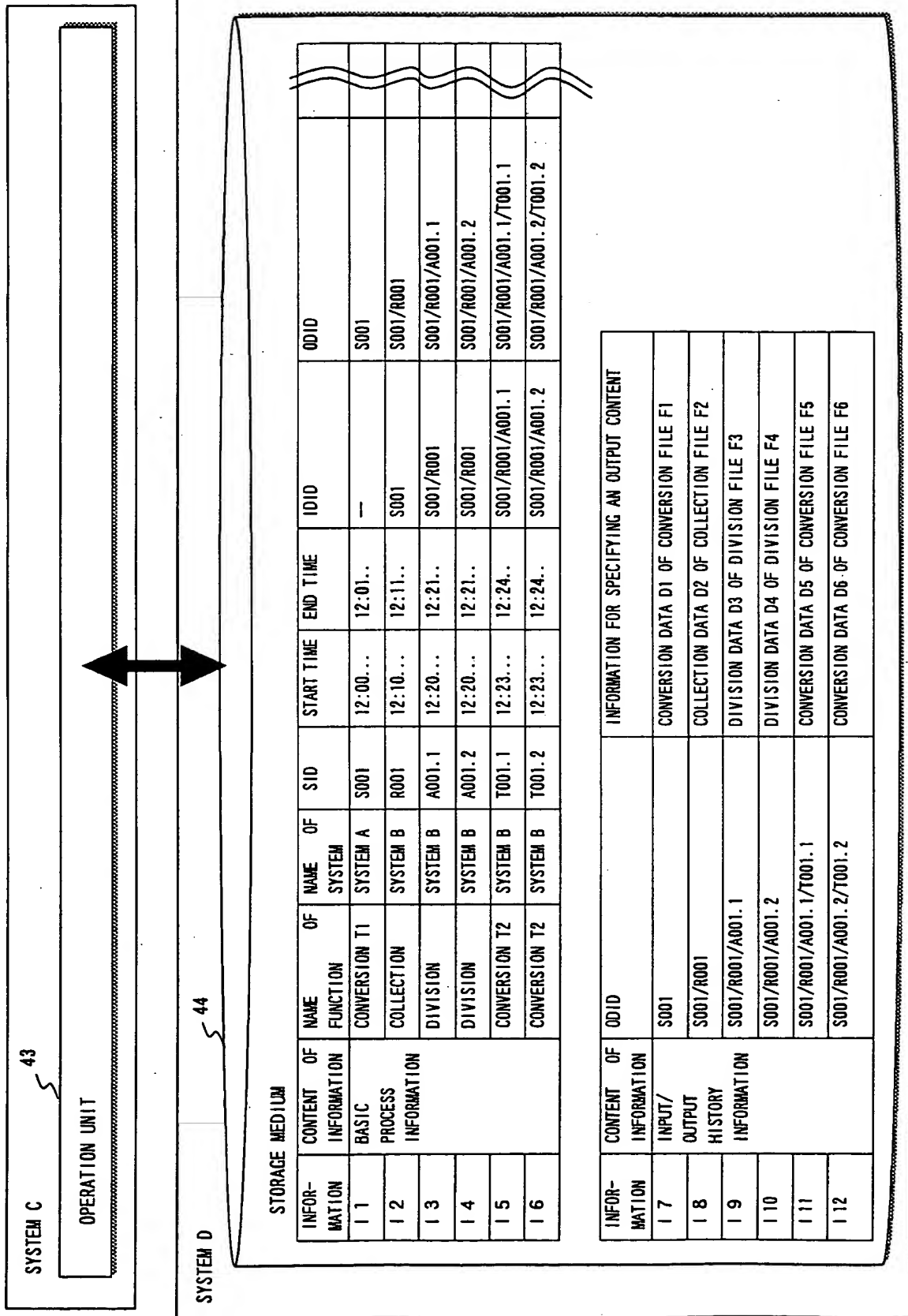


FIG. 5

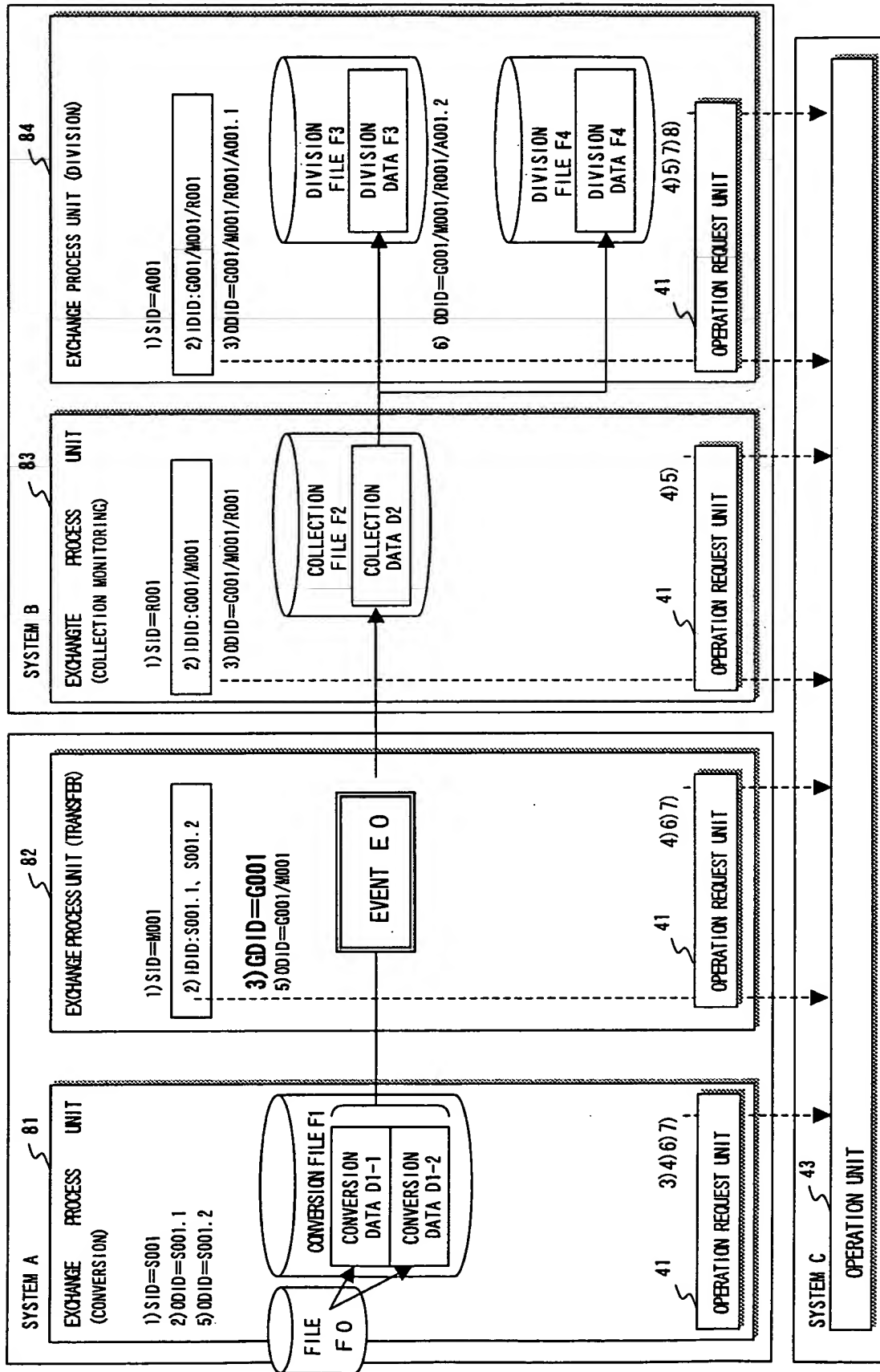


FIG. 6

SYSTEM C 43

OPERATION UNIT

SYSTEM D 44

STORAGE MEDIUM

INFOR- MATION	CONTENT OF INFORMATION	NAME OF SYSTEM	SID	START TIME	END TIME	IDID	ODID
I 1	BASIC	CONVERSION T1	S001.1	12:00...	12:01..	—	S001.1
I 2	PROCESS	CONVERSION T2	S001.2	12:00...	12:01..	—	S001.2
I 3	INFORMATION	TRANSFER	M001	12:10...	12:11..	G001	G001/M001
I 4		COLLECTION MONITORING	R001	12:20...	12:21..	G001/M001	G001/M001/R001
I 5		DIVISION	A001.1	12:30...	12:31..	G001/M001/R001	G001/M001/R001/A001.1
I 6		DIVISION	A001.2	12:30...	12:31..	G001/M001/R001	G001/M001/R001/A001.2

INFOR- MATION	CONTENT OF INFORMATION	ODID	INFORMATION FOR SPECIFYING AN OUTPUT CONTENT
I 7	INPUT/OUTPUT	S001.1	CONVERSION DATA D1-1 OF CONVERSION FILE F1
I 8	HISTORY	S001.2	CONVERSION DATA D1-2 OF CONVERSION FILE F1
I 9	INFORMATION	G001/M001	EVENT E 0
I 10		G001/M001/R001	COLLECTION DATA D2 OF COLLECTION FILE F2
I 11		G001/M001/R001/A001.1	DIVISION DATA D3 OF DIVISION FILE F3
I 12		G001/M001/R001/A001.2	DIVISION DATA D4 OF DIVISION FILE F4

INFORMATION	CONTENT OF INFORMATION	GDID	GROUPED INPUT/OUTPUT IDENTIFIER
I 13	INPUT GROUP INFORMATION	G001	S001.1, S001.2

FIG. 7

SYSTEM D

44

INFORMATION	CONTENT OF INFORMATION	NAME OF FUNCTION	NAME OF SYSTEM	SID	START TIME	END TIME	IDID	ODID
I 1	BASIC PROCESS INFORMATION	CONVERSION T1	SYSTEM A	S001	12:00...	12:01..	--	S001
I 2		COLLECTION	SYSTEM B	R001	12:10...	12:11..	S001	S001/R001
I 3		DIVISION	SYSTEM B	A001.1	12:20...	12:21..	S001/R001	S001/R001/A001.1
I 4		DIVISION	SYSTEM B	A001.2	12:20...	12:21..	S001/R001	S001/R001/A001.2
I 5		CONVERSION T2	SYSTEM B	T001.1	12:23...	12:24..	S001/R001/A001.1	S001/R001/A001.1/T001.1
I 6		CONVERSION T2	SYSTEM B	T001.2	12:23...	12:24..	S001/R001/A001.2	S001/R001/A001.2/T001.2

INFORMATION	CONTENT OF INFORMATION	ODID	INFORMATION FOR SPECIFYING AN OUTPUT CONTENT
I 7	INPUT/OUTPUT HISTORY INFORMATION	S001	CONVERSION DATA D1 OF CONEVERSION FILE F1
I 8		S001/R001	COLLECITON DATA D2 OF COLLECTION FILE F2
I 9		S001/R001/A001.1	DIVISION DATA D3 OF DIVISION FILE F3
I 10		S001/R001/A001.2	DIVISION DATA D4 OF DIVISION FILE F4
I 11		S001/R001/A001.1/T001.1	CONVERSION DATA D5 OF CONVERSION FILE F5
I 12		S001/R001/A001.2/T001.2	CONVERSION DATA D6 OF CONVERSION FILE F6

SYSTEM E

43

OPERATION UNIT

FIG. 8

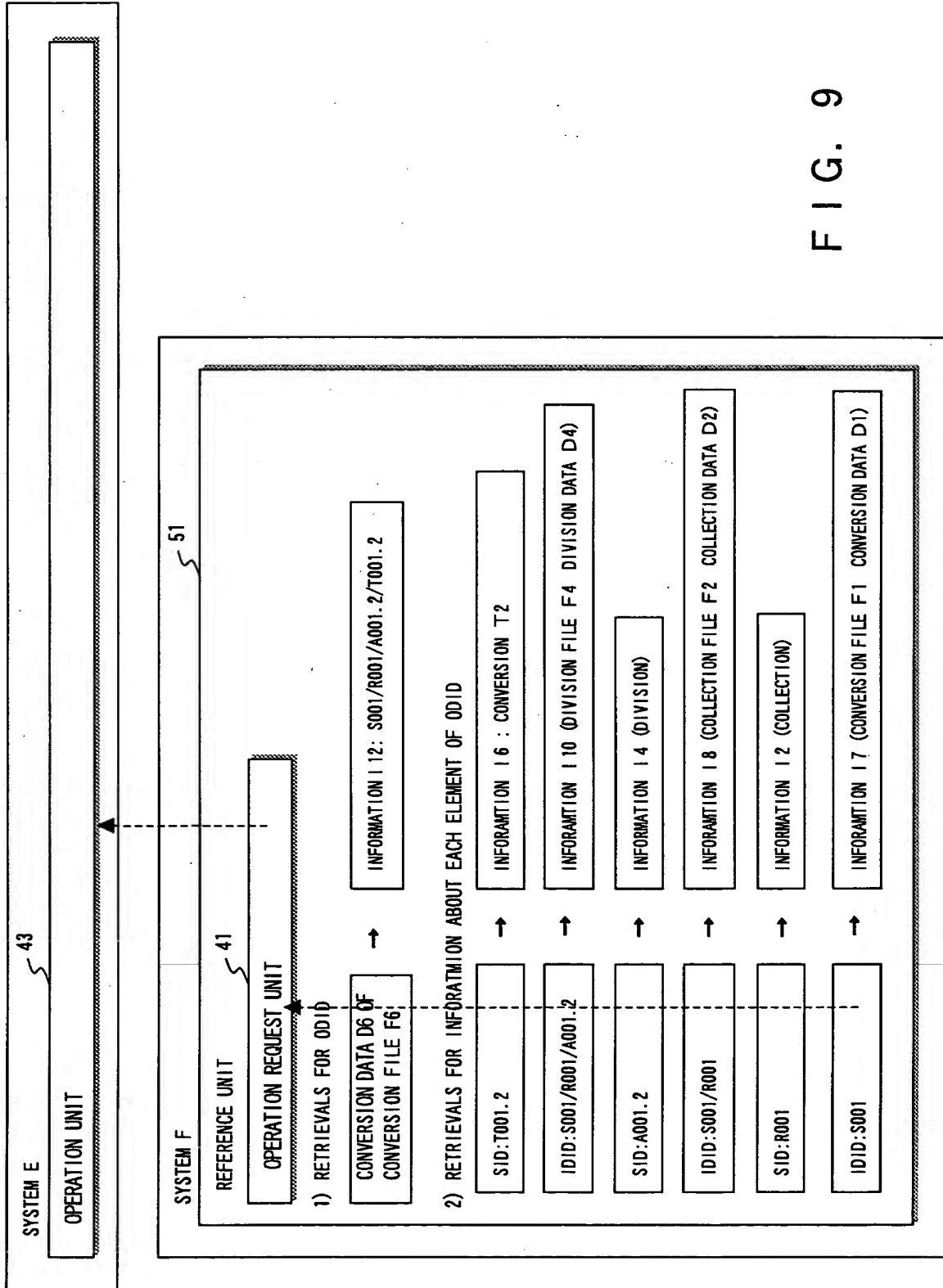


FIG. 9

BASIC PROCESS FUNCTION	NAME OF FUNCTION	NAME OF SYSTEM	START TIME	END TIME
	TRANSFER	SYSTEM A	12:01.30	12:01.32
	COLLECTION	SYSTEM B	14:10.15	14:11.01
	TRANSFER	SYSTEM A	14:21.30	14:21.42
	COCCECTION	SYSTEM B	16:15.15	16:15.14

FIG. 10

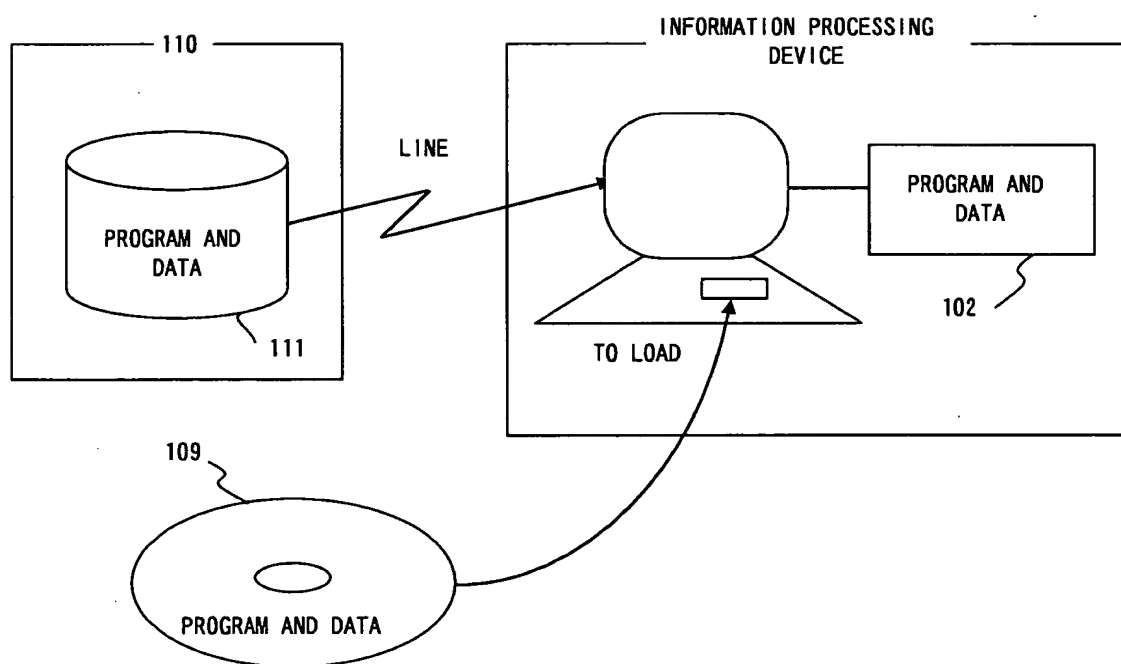
CONTENT OF INFORMATION	ODID	INFORMATION FOR SPECIFYING AN OUTPUT CONTENT
INPUT/OUTPUT HISTORY INFORMATION	S001/R001	COLLECTION DATA D2 OF COLLECTION FILE F2

CONTENT OF INFORMATION	NAME OF FUNCTION	START TIME	END TIME	IDID	ODID
BASIC PROCESS INFORMATION	CONVERSION T 1	12:00...	12:01..	--	S001
	COLLECTION	12:10...	12:11..	S001	S001/R001
	DIVISION	12:20...	12:21..	S001/R001	S001/R001/A001.1
	DIVISION	12:20...	12:21..	S001/R001	S001/R001/A001.2
	CONVERSION T 2	12:23...	12:24..	S001/R001/A001.1	S001/R001/A001.1/T001.1
	CONVERSION T 2	12:23...	12:24..	S001/R001/A001.2	S001/R001/A001.2/T001.2

FIG. 11

Figure 1 is a block diagram of a computer system architecture. A central vertical line represents the **BUS**. On the left side of the bus, five components are connected: **CPU** (101), **MEMORY** (102), **INPUT DEVICE** (103), **OUTPUT DEVICE** (104), and **EXTERNAL STORAGE DEVICE** (105). On the right side of the bus, three components are connected: **MEDIUM DRIVE DEVICE** (106), **NETWORK CONNECTION DEVICE** (107), and **PORTABLE STORAGE** (109). The **PORTABLE STORAGE** (109) is connected to the **MEDIUM DRIVE DEVICE** (106) via a bidirectional arrow. The **NETWORK CONNECTION DEVICE** (107) is connected to the **NETWORK** via a bidirectional arrow.

FIG. 12



F I G. 13